

12354

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT

APPLICANTS: Tilmann Schilling et al. CONFIRMATION NO.: 7439
SERIAL NO.: 10/614,280 GROUP ART UNIT: 2854
FILED: 07/07/2003
TITLE: METHOD FOR CONTROLLING PRINTING IN A MAIL-
PROCESSING DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

S I R:

In accordance with the provisions of 37 C.F.R. § 1.56, Applicants request that citation and examination of the following documents be made during the course of examination of the above-referenced application for United States Letters Patent.

AA United States Patent No. 6,041,704
AB United States Patent No. 5,953,426
AC United States Patent No. 5,790,768
AD United States Patent No. 5,710,721
AE United States Patent No. 5,707,158
AF United States Patent No. 5,651,103
AG United States Patent No. 5,608,636
AH United States Patent No. 5,155,499
AI United States Patent No. 5,117,374
AJ United States Patent No. 4,978,971
AL European Application 1 176 016
AM European Application 1 154 382
AN European Application 1 132 868

AO European Application 921 009
AP European Application 921 008
AQ European Application 762 334
AT XILINX, "Spartan-II 2.5 V FPGA Family: Functional
Description" March 5, 2001, pgs 1-45

EXPLANATION OF RELEVANCE

References AA through AG, AL through AQ, and AT were identified and discussed in the present application, and Applicants stand by the statements in the specification concerning the teachings of those references.

The German counterpart to reference AD, together with references AH, AI and AJ, were cited by the German Patent and Trademark Office during examination of the counterpart German application. Since all of these references are in English, no further comment and concern of the teachings is necessary.

Copies of each of the above references together with Form 1449 are submitted herewith.

As of the date of mailing of this Information Disclosure Statement, a first Office Action on the merits has not been received in connection with this application, and therefore, this Information Disclosure Statement is in compliance with 37 C.F.R. §1.97(b)(3), and no fee is necessary.

All claims of the application are submitted to be patentable over the teachings of the above references, taken singly or in combination. Early consideration of the application is therefore respectfully requested.

Submitted by/



(Reg. 28,982)

SCHIFF, HARDIN & WAITE

CUSTOMER NO. 26574

Patent Department

6600 Sears Tower

233 South Wacker Drive

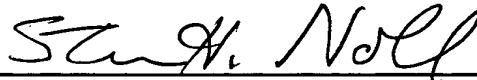
Chicago, Illinois 60606

Telephone: 312/258-5790

Attorneys for Applicant.

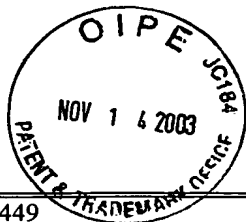
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on November 12, 2003.



STEVEN H. NOLL

CH1\ 4070409.1



Form PTO-1449

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**
(use several sheets if necessary)

Docket No.
P03,0158

Serial No.
10/614,280

Applicant
Tilmann Schilling et al.

Filing Date
07/07/2003

Group Art Unit
2854

U.S. PATENT DOCUMENTS

Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
	AA	6,041,704	03/28/2000	Pauschinger			
	AB	5,953,426	09/14/99	Windel et al.			
	AC	5,790,768	08/04/98	Windel et al.			
	AD	5,710,721	01/20/98	Rieckoff et al.			
	AE	5,707,158	01/13/98	Hansel et al.			
	AF	5,651,103	07/22/97	Arsenault et al.			
	AG	5,608,636	03/04/97	Guenther			
	AH	5,155,499	10/13/92	Goetz et al.			
	AI	5,117,374	05/26/92	Goetz			
	AJ	4,978,971	12/18/90	Goetz et al.			
	AK						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL	1 176 016	01/30/2002	Europe				
	AM	1 154 382	11/14/2001	Europe				
	AN	1 132 868	09/12/2001	Europe				
	AO	921 009	06/09/99	Europe				
	AP	921 008	06/09/99	Europe				
	AQ	762 334	03/12/97	Europe				
	AR							
	AS							

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AT	XILINX, "Spartan-II 2.5 V FPGA Family: Functional Description" March 5, 2001, pgs 1-45
	AU	
	AV	
	AW	

Examiner

Date Considered

***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.